

IN THE CLAIMS

1. (cancelled)

2. (cancelled)

3. (currently amended) A ~~The computer according to claim 2, further comprising:~~

means for generating a first surface region that represents a first operand of an instruction and a second surface region that represents a second operand of the instruction and for reading out a first set of surface region data that corresponds to the first surface region and a second set of surface region data that corresponds to the second surface region;

means for operating in parallel on the first and second sets of surface region data in accordance with the instruction; and

means for changing surface region size such that when the first and second surface regions do not have the same size, the size of at least one of the first and second surface regions is changed before operating on the first and second sets of surface region data so that the size of a given one of the first and second surface regions corresponds at a predetermined scale ratio to the size of another of the first and second surface regions.

4. (previously presented) The computer according to claim 3, wherein said means for changing surface region size enlarges or reduces the size of the given one of the first and second surface regions relative to the size of the another of the first and second surface regions.

5. (previously presented) The computer according to claim 3, wherein said means for changing surface region size enlarges or reduces the size of the first and second surface regions in relation to the size of a further surface region.

6. (previously presented) The computer according to claim 5, wherein the further surface region is a surface region representing a destination operand of the instruction.

7. (previously presented) The computer according to claim 5, wherein said means for changing surface region size enlarges or reduces the size of the first and second surface regions so that the size of each of the first and second surface regions is equal to the size of the further surface region.

8. (previously presented) The computer according to claim 3, wherein said means for changing surface region size performs copy processing or performs interpolation processing to generate data corresponding to a specific address in the given one of the first and second surface regions.

9. (previously presented) The computer according to claim 8, wherein the interpolation processing is carried out by operations selected from the group consisting of bilinear processing, tri-linear processing, mask processing, point sampling processing and pattern matching processing.

10. (previously presented) A computer, comprising:
a memory operable to store surface region data;
a preprocessor operable to generate a first surface region that represents a first operand of an instruction and a second surface region that represents a second operand of the instruction and to read out from said memory a first set of surface region data that corresponds to the first surface region

and a second set of surface region data that corresponds to the second surface region;

an address generator operable to change surface region size such that, when the first and second surface regions do not have the same size, the size of at least one of the first and second surface regions is changed so that the size of a given one of the first and second surface regions corresponds at a predetermined scale ratio to the size of another of the first and second surface regions; and

a processor array operable to perform parallel operations on the first and second sets of surface region data in accordance with the instruction.

11. (previously presented) The computer according to claim 10, wherein said address generator enlarges or reduces the size of the given one of the first and second surface regions so that the size of the given one of the first and second surface regions equals the size of the another of the first and second surface regions.

12. (previously presented) The computer according to claim 10, wherein said address generator performs copy processing or performs interpolation processing to generate data corresponding to a specific address in the given one of the first and second surface regions.

13. (previously presented) The computer according to claim 12, wherein the interpolation processing is carried out by operations selected from the group consisting of bilinear processing, tri-linear processing, mark processing, point sampling processing and pattern matching processing.

14. (previously presented) The computer according to claim 10, wherein said address generator enlarges or reduces

the size of the first and second surface regions in relation to the size of a further surface region.

15. (previously presented) The computer according to claim 14, wherein the further surface region is a surface region representing a destination operand of the instruction.

16. (previously presented) The computer according to claim 14, wherein said address generator enlarges or reduces the size of the first and second surface regions so that the size of each of the first and second surface regions is equal to the size of the further surface region.